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(71) Applicant (for DE only): PHILIPS INTELLECTUAL  
PROPERTY & STANDARDS GMBH [DE/DE]; Stein-  
damm 94, 20099 Hamburg (DE).

(71) Applicant (for all designated States except DE, US):  
KONINKLIJKE PHILIPS ELECTRONICS N. V.  
[NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven  
(NL).

(72) Inventors; and

(75) Inventors/Applicants (for US only): UNGERMANN,

Jörn [DE/DE]; c/o Philips Intellectual Property & Standards GmbH, Weisshausstr. 2, 52066 Aachen (DE). FUHRMANN, Peter [DE/DE]; c/o Philips Intellectual Property & Standards GmbH, Weisshausstr. 2, 52066 Aachen (DE).

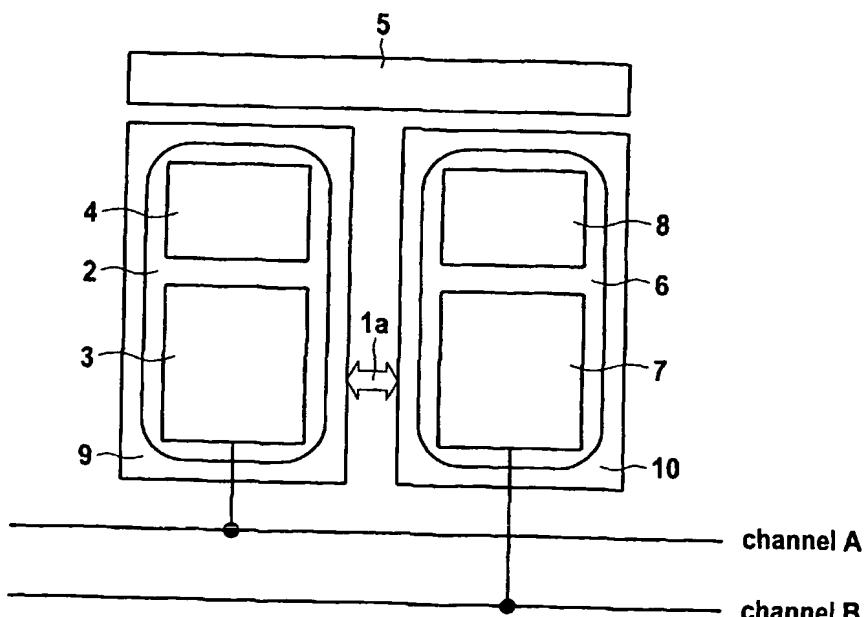
(74) Agent: VOLMER, Georg; Philips Intellectual Property & Standards GmbH, Weisshausstr. 2, 52066 Aachen (DE).

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*[Continued on next page]*

(54) Title: TIME-TRIGGERED COMMUNICATION SYSTEM AND METHOD FOR THE SYNCHRONIZED START OF A DUAL-CHANNEL NETWORK



(57) Abstract: A dual-channel network with in each case one communication controller (2, 6) for each of the two channels (A, B). In order to ensure that the two channels (A, B) operate on a temporally matched basis, an exchange of current states ("ready, "abort") takes place via an external or an on-chip interface (1a, 1b). The cold start operation is carried out only if, and so long as, both communication controllers are in the "ready" state.



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# INTERNATIONAL SEARCH REPORT

International Application No  
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**A. CLASSIFICATION OF SUBJECT MATTER**  
IPC 7 H04L12/40 H04J3/06 G06F11/20 H04L7/10 G06F11/16

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)  
IPC 7 H04L H04J G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EP0-Internal

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>US 6 467 003 B1 (TOPIC MICHAEL ET AL) 15 October 2002 (2002-10-15)</p> <p>abstract</p> <p>column 1, line 61</p> <p>column 31, line 65</p> <p>column 6, line 5</p> <p>column 23, line 5</p> <p>column 23, line 20</p> <p>figure 4</p> <p>-----</p> <p>ANONYMOUS: "Chip set PSS SB CHIPSETO" SAFE BUS SYSTEMS, [Online] 22 August 2000 (2000-08-22), XP002294998</p> <p>Retrieved from the Internet: URL:www.pilz.jp/pdf/downloads/pss_sb_chips et0.pdf&gt; [retrieved on 2004-09-06]</p> <p>figure 1</p> <p>-----</p> <p style="text-align: right;">-/-</p>	1-19
A		6

Further documents are listed in the continuation of box C.

Patent family members are listed in annex.

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- "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
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Date of the actual completion of the international search

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Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2  
NL - 2280 HV Rijswijk  
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,  
Fax: (+31-70) 340-3016

Authorized officer

Schneider, G

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 0 447 576 A (TANDEM COMPUTERS INC) 25 September 1991 (1991-09-25) column 15, line 43 -----	5
A	JOHN RUSHBY: "A Comparison of Bus Architectures for Safety-Critical Embedded Systems" INTERNET ARTICLE, [Online] 1 March 2003 (2003-03-01), pages 1-52, XP002294999 Retrieved from the Internet: URL:techreports.larc.nasa.gov/ ltrs/PDF/2003/cr/NASA-2003-cr212161.pdf> [retrieved on 2004-09-06] the whole document -----	

# INTERNATIONAL SEARCH REPORT

## Information on patent family members

International Application No  
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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 6467003	B1	15-10-2002 US 6052753 A	18-04-2000
EP 0447576	A	25-09-1991 EP 0447576 A1	25-09-1991